



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Stephen M. Douglass
Assignor: Xilinx, Inc.
Title: "Method of Designing Integrated Circuit Having Both Configurable and Fixed Logic Circuitry"
Serial No.: 10/092,051 File Date: 03-04-02
Examiner: Unknown Art Unit: 2811
Docket No.: X-923 US Conf. No.: 7802

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Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the twelve (12) references listed in the attached Substitute for form 1449A/PTO. A copy of each is enclosed herein.

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Respectfully submitted,

H. C. Chan
Attorney for Applicant
Reg. No. 35,477

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231, on July 16, 2002.

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PTO/SB/08A (10-01)
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Substitute for form 1041-49A/RP

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Sheet	1	of	2
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Applicati n / C nf. No.	10/092,051 / 7802
Filing Date	March 04, 2002
First Named Inventor	Stephen M. Douglass
Art Unit	2811
Examiner Name	Unknown
Attorney Docket Number	X-923 US

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner
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¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Attorney Docket Number	X-923 US

(use as many sheets as necessary)

Sheet	2	of	2
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OTHER -- NON PATENT LITERATURE DOCUMENTS

**Examiner
Signature**

Date Considered

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